A Satisfiability Encoding of Dependency Pair Techniques for Maximal Completion

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Abstract

We present a general approach to encode termination in the dependency pair framework as a satisfiability problem, and include encodings of dependency graph and reduction pair processors. We use our encodings to increase the power of the completion tool Maxcomp.

1 Introduction

Maximal completion [4] is a simple yet efficient Knuth-Bendix completion approach which relies on MaxSAT solving. It can thus only compute convergent term rewrite systems (TRSs) whose termination can be expressed by satisfiability constraints.

Encoding termination techniques for TRSs via satisfiability problems has become common practice. However, to the best of our knowledge all previous encodings restrict to a single termination technique such as a specific reduction order or interpretations into a particular domain. Hence the maximal completion tool Maxcomp was so far restricted to LPO and KBO, and could not handle input problems such as CGE2 [6], which describes two commuting group endomorphisms as given by the following set of equations E:

\[
\begin{align*}
e \cdot x & \approx x \\
i(x) \cdot x & \approx e \\
f(x \cdot y) & \approx f(x) \cdot f(y) \\
g(x \cdot y) & \approx g(x) \cdot g(y) \\
(x \cdot y) \cdot z & \approx (x \cdot y) \cdot z \\
f(x) \cdot g(y) & \approx g(y) \cdot f(x)
\end{align*}
\]

In this paper we present a uniform layout for an SMT encoding of compound termination strategies that combine different techniques from the dependency pair framework. We give encodings of dependency pairs, a rule removal processor, and two versions of dependency graph approximations. We implemented our encodings on top of Maxcomp. Our experimental results show that this allows Maxcomp to complete problems like CGE2, and boosts its power beyond simple termination.

2 Preliminaries

We assume familiarity with term rewriting [1]. Knuth-Bendix completion aims to transform an equational system (ES) \(E\) into a TRS \(R\) which is convergent for \(E\), i.e., terminating, confluent and equivalent to \(E\). We write \(\text{CP}(R)\) for the set of critical pairs of a TRS \(R\), and \(\downarrow_R\) for \(\rightarrow_R^*\). Maximal completion is a simple completion approach based on MaxSAT solving. For an input ES \(E\), it tries to compute \(\varphi(E)\) where \(\varphi\) is defined as

\[
\varphi(C) = \begin{cases} 
R & \text{if } E \cup \text{CP}(R) \subseteq \downarrow_R \text{ for some } R \in \mathcal{R}(C) \\
\varphi(C \cup S(C)) & \text{otherwise}
\end{cases}
\]

\(\mathcal{R}(C)\) consists of terminating TRSs \(R\) such that \(R \subseteq C \cup C^{-1}\), and \(S(C) \subseteq \bigcup_{R \in \mathcal{R}(C)} \text{CP}(R)\).
Theorem 1 ([4]). The TRS \( \varphi(\mathcal{E}) \) is convergent for \( \mathcal{E} \) if it is defined.

In the maximal completion tool Maxcomp, \( \mathcal{R}(C) \) is computed by maximizing the number of satisfied clauses in \( \bigvee_{s \leq t \in \mathcal{E}} [s > t] \lor [t > s] \), subject to the side constraints implied by the SAT/SMT encoding \([ > > ]\) of some reduction order \( > \).

In this paper we use the dependency pair (DP) framework to show termination of TRSs [3]. A DP problem is a pair of two TRSs \((\mathcal{P}, \mathcal{R})\), it is finite if it does not admit an infinite chain. A DP processor \(\text{Proc} \) is a function which maps a DP problem to either a set of DP problems or “no”. It is sound if a DP problem \(d\) is finite whenever \(\text{Proc}(d) = \{d_1, \ldots, d_n\}\) and all of \(d_i\) are finite.

For an ES \(\mathcal{C}\), we define the set of dependency pair candidates \(\text{DPC}(\mathcal{C})\) as all rules \(\ell^\# \rightarrow u^\#\) such that \(\ell \approx r \in \mathcal{C}, \ell \rightarrow r\) is a rewrite rule, and \(r \geq u\) but \(\ell \not\approx u\).

3 Encodings

We first illustrate the idea of our encodings by means of an example.

Example 2. Suppose we want to orient a maximal number of equations from the ES \(\mathcal{E}\) given in the introduction, where termination is to be shown by computing dependency pairs, applying a reduction pair processor based on a polynomial interpretation and finally a reduction pair processor based on LPO with argument filterings.

Let \(\mathcal{P} = \text{DPC}(\mathcal{E} \cup \mathcal{E}^{-1})\). For all equations \(s \approx t \in \mathcal{C} = \mathcal{E} \cup \mathcal{E}^{-1} \cup \mathcal{P}\) we use strict variables \(S_i^{s \rightarrow t}\) as well as weak variables \(W_i^{s \rightarrow t}\) for all \(0 \leq i \leq 3\). Moreover, boolean variables \(X_i^{\text{def}}\) encode whether \(f\) is a defined symbol. We maximize the number of satisfied clauses in the disjunction \(\bigvee_{s \leq t \in \mathcal{E}} S_i^{s \rightarrow t} \lor S_0^{s \rightarrow t}\) subject to the following constraints:

\[
\begin{align*}
\bigwedge_{s \leq t \in \mathcal{E}} S_i^{s \rightarrow t} & \rightarrow (W_1^{s \rightarrow t} \land X_3^{\text{root}(s)}) \land \bigwedge_{\ell \rightarrow r \in \text{DPC}(s \rightarrow t)} X_1^{\text{root}(r)} \rightarrow S_1^{\ell \rightarrow r} \quad \text{(a)} \\
\bigwedge_{i=2}^3 \bigwedge_{\ell \rightarrow r \in \mathcal{P}} (S_i^{\ell \rightarrow r} \rightarrow [\ell \geq^i r]) \land (\neg [\ell >^i r] \rightarrow S_i^{\ell \rightarrow r}) \quad \text{(b)} \\
\bigwedge_{i=2}^3 \bigwedge_{s \leq t \in \mathcal{E}} W_i^{s \rightarrow t} \rightarrow (W_i^{t \rightarrow s} \land [s \geq^i t]) \quad \text{(c)} \\
\bigwedge_{\ell \rightarrow r \in \mathcal{P}} \neg S_3^{\ell \rightarrow r} \quad \text{(d)}
\end{align*}
\]

Clauses (a) trigger DPs and ‘move’ rules to the weak component, (b) expresses that if a DP is not oriented it remains to be considered, (c) requires rules to be weakly oriented, and (d) demands that finally no DP remains unoriented. Here \([\ell >^i r]\) \([\ell \geq^i r]\) refers to strict (weak) orientation constraints imposed by polynomial interpretations for \(i = 2\) and LPO with argument filterings for \(i = 3\).

The following paragraphs transfer standard notions of the DP framework to our satisfiability setting. A DP problem encoding is a tuple \(\mathcal{D} = (\mathcal{S}, \mathcal{W}, \phi)\) consisting of two sets of boolean variables \(\mathcal{S} = \{S_{\ell \rightarrow r} \mid \ell \rightarrow r \in \mathcal{P}\}\) and \(\mathcal{W} = \{W_{\ell \rightarrow r} \mid \ell \rightarrow r \in \mathcal{R}\}\) for TRSs \(\mathcal{P}\) and \(\mathcal{R}\), and a formula \(\phi\). An assignment \(\alpha\) is finite for a DP problem encoding \(\mathcal{D} = (\mathcal{S}, \mathcal{W}, \phi)\) if \(\alpha(\phi) = \top\) and the DP problem \((\mathcal{P}_\alpha^S, \mathcal{R}_\alpha^W)\) given by the TRSs

\[
\begin{align*}
\mathcal{P}_\alpha^S = \{ \ell \rightarrow r \mid S_{\ell \rightarrow r} \in \mathcal{S}, \alpha(S_{\ell \rightarrow r}) = \top \} \\
\mathcal{R}_\alpha^W = \{ \ell \rightarrow r \mid W_{\ell \rightarrow r} \in \mathcal{W}, \alpha(W_{\ell \rightarrow r}) = \top \}
\end{align*}
\]
is finite. A DP processor encoding Proc maps a DP problem encoding \(\mathcal{D} = (\mathcal{S}, \mathcal{W}, \phi)\) to a finite set of DP problem encodings Proc(\(\mathcal{D}\)) = \{\mathcal{D}_1, \ldots, \mathcal{D}_n\}. A DP processor encoding Proc is sound if for any \(\mathcal{D}\) such that Proc(\(\mathcal{D}\)) = \{\mathcal{D}_1, \ldots, \mathcal{D}_n\} and any assignment \(\alpha\) that is finite for all \(\mathcal{D}_i\), it also holds that \(\alpha\) is finite for \(\mathcal{D}\).

For an ES \(C\) its set of initial variables is \(\mathcal{I}_C = \{\ell \approx r \in C\}\).

**Definition 3.** For an ES \(C\) with initial variables \(\mathcal{I}_C\) the initial DP problem encoding is given by \(\mathcal{D}_C = (\mathcal{S}, \mathcal{W}, \phi)\) where \(\mathcal{S} = \{\mathcal{S}_{\ell \rightarrow r} \mid \ell \rightarrow r \in \text{DPC}(C)\}\), \(\mathcal{W} = \{\mathcal{W}_{\ell \rightarrow r} \mid \ell \approx r \in C\}\) and

\[
\phi = \bigwedge_{\ell \approx r \in C} I_{\ell \rightarrow r} \rightarrow \left( W_{\ell \rightarrow r} \land X^{\text{def}}_{\text{root}(\ell)} \land \bigwedge_{s \rightarrow t \in \text{DPC}(\ell \rightarrow r)} X^{\text{def}}_{\text{root}(t)} \rightarrow S_{s \rightarrow t} \right)
\]

**Lemma 4.** Let \(C\) be an ES. Suppose there is a tree whose nodes are labelled with DP problem encodings satisfying the following conditions:
- The root is labelled with the initial DP problem encoding \(\mathcal{D}_C\).
- For every non-leaf node labelled \(\mathcal{D}\) with \(n\) children labelled \(\mathcal{D}_1, \ldots, \mathcal{D}_n\) there is a sound processor encoding Proc such that Proc(\(\mathcal{D}\)) = \{\mathcal{D}_1, \ldots, \mathcal{D}_n\}.

Let the leaves be labelled \(\{(\mathcal{S}_i, \mathcal{W}_i, \phi_i) \mid 1 \leq i \leq k\}\). If the formula

\[
\phi = \bigwedge_{i=1}^{k} \phi_i \land \bigwedge_{s \rightarrow t \in \mathcal{S}_i} \neg S_{s \rightarrow t}
\]

is satisfied by an assignment \(\alpha\) then the TRS \(R = \{\ell \rightarrow r \mid \alpha(I_{\ell \rightarrow r}) = T\}\) is terminating.

**Proof.** By induction on the tree structure, \(\alpha\) is finite for all DP problem encodings occurring as labels. Termination of \(R\) follows from finiteness of \(\alpha\) for the root label \(\mathcal{D}_C\).

**Definition 5 (Reduction pair processor).** Let \(\prec, \succ\) be a reduction pair and \(\pi\) an argument filtering, with satisfiability encodings \([\cdot \geq_\pi \cdot]\) and \([\cdot >_\pi \cdot]\).

A DP problem encoding \((\mathcal{S}, \mathcal{W}, \phi)\) is mapped to \(\{(\mathcal{S}', \mathcal{W}', \phi \land T_S \land T_W)\}\) where \(\mathcal{S}' = \{\mathcal{S}_{\ell \rightarrow r} \mid \mathcal{S}_{\ell \rightarrow r} \in \mathcal{S}\}\), \(\mathcal{W}' = \{\mathcal{W}_{\ell \rightarrow r} \mid \mathcal{W}_{\ell \rightarrow r} \in \mathcal{W}\}\),

\[
T_S = \bigwedge_{\mathcal{S}_{\ell \rightarrow r} \in \mathcal{S}} \mathcal{S}_{\ell \rightarrow r} \rightarrow [\ell \geq_\pi r] \land \neg[\ell \succ_\pi r] \rightarrow \mathcal{S}'_{\ell \rightarrow r}
\]

\[
T_W = \bigwedge_{\mathcal{W}_{\ell \rightarrow r} \in \mathcal{W}} \mathcal{W}_{\ell \rightarrow r} \rightarrow \mathcal{W}'_{\ell \rightarrow r} \land [\ell \geq_\pi r]
\]

Concrete encodings \([\cdot \geq_\pi \cdot]\) and \([\cdot >_\pi \cdot]\) for LPO/RPO, KBO as well as reduction orders given by polynomial and matrix interpretations—also in combination with argument filterings and usable rules—are well-studied, see for instance [5, 9, 2, 8].

Note that Definition 5 can easily be modified to admit rule removal by setting

\[
T_W = \bigwedge_{\mathcal{W}_{\ell \rightarrow r} \in \mathcal{W}} \mathcal{W}_{\ell \rightarrow r} \rightarrow [\ell \geq_\pi r] \land \neg[\ell >_\pi r] \rightarrow \mathcal{W}'_{\ell \rightarrow r}
\]

**Definition 6 (Dependency graph processor).** A DP problem encoding \((\mathcal{S}, \mathcal{W}, \phi)\) is mapped to the set \(\{(\mathcal{S}', \mathcal{W}', \psi)\}\) such that \(\mathcal{S}' = \{\mathcal{S}_{\ell \rightarrow r} \mid \mathcal{S}_{\ell \rightarrow r} \in \mathcal{S}\}\), \(\mathcal{W}' = \{\mathcal{W}_{\ell \rightarrow r} \mid \mathcal{W}_{\ell \rightarrow r} \in \mathcal{W}\}\), and \(\psi = \phi \land T_S \land T_W\) where

\[
T_S = \bigwedge_{S_{p_1} \land S_{p_2} \in \mathcal{S}} S_{p_1} \land S_{p_2} \land [\text{edge} \ p_1 \rightarrow p_2] \land \neg S'_{p_1} \land \neg S'_{p_2} \rightarrow X^{\text{w}}_{p_1} > X^{\text{w}}_{p_2}
\]

\[
T_W = \left( \bigwedge_{\mathcal{S}_{\ell \rightarrow r} \in \mathcal{S}} \mathcal{S}_{\ell \rightarrow r} \rightarrow \mathcal{S}'_{\ell \rightarrow r} \right) \land \left( \bigwedge_{\mathcal{W}_{\ell \rightarrow r} \in \mathcal{W}} \mathcal{W}_{\ell \rightarrow r} \rightarrow \mathcal{W}'_{\ell \rightarrow r} \right)
\]
Here $T_S\downarrow$ encodes cycle analysis of the graph in the sense that a cycle $p_1 \to p_2 \to \cdots \to p_n \to p_1$ issues the unsatisfiable constraint $X_{p_1}^w > X_{p_2}^w > \cdots > X_{p_n}^w > X_{p_1}^w$. For the formula $[s \to t \overset{\text{edge}}{\longrightarrow} u \to v]$ encoding the presence of an edge from $s \to t$ to $u \to v$ one can simply use $\top$ if $\text{root}(t) = \text{root}(u)$ and $\bot$ otherwise. (We also experimented with an encoding in terms of the unifiability between $\text{REN}(\text{CAP}(t))$ and $u$, but due to reasons of space do not presented here.)

The above encoding does not allow to use different orderings in SCCs, in contrast to what is commonly done in termination provers. However, it can be modified to consider SCCs by mapping a problem encoding to $k$ independent problem encodings.

**Definition 7** (Dependency graph processor with $k$ SCCs). A DP problem encoding $D = (S,W,\phi)$ is mapped to $\{D_i\}_{1 \leq i \leq k} = \{(S_i,W_i,\psi_i)\}_{1 \leq i \leq k}$ where $S_i = \{S_{i,t \to r} \mid S_{t \to r} \in S\}$, $W_i = \{W_{i,t \to r} \mid W_{t \to r} \in W\}$, $\psi_i = \phi \land T_{\text{sc}}(k) \land T_S(i) \land T_W(i)$, and

$$T_{\text{sc}}(k) = \bigwedge_{S_p \in S} 1 \leq X_p^{\text{sc}} \leq k \land \bigwedge_{S_{p_1},S_{p_2} \in S} S_{p_1} \land S_{p_2} \land [p_1 \overset{\text{edge}}{\longrightarrow} p_2] \rightarrow X_{p_1,p_2}^{\text{edge}} \land X_{p_1}^{\text{sc}} \geq X_{p_2}^{\text{sc}}$$

$$T_S(i) = \bigwedge_{S_{p_1},S_{p_2} \in S} X_{p_1,p_2}^{\text{edge}} \land X_{p_1}^{\text{sc}} = i \land X_{p_2}^{\text{sc}} = i \land \neg S_{i,p_1} \land \neg S_{i,p_2} \rightarrow X_{p_1}^w > X_{p_2}^w$$

$$T_W(i) = \bigwedge_{W_p \in W} W_{i,p} \rightarrow W_{i,p} \land \bigwedge_{S_p \in S} S_p \land X_p^{\text{sc}} = i \land \left( \bigvee_{S' \in S \setminus \{S_p\}} X_{p'}^{\text{sc}} = X_{p'}^{\text{sc}} \right) \rightarrow W_{i,p}$$

Here $X_{p_1,p_2}^{\text{edge}}$ is a boolean variable encoding the presence of both DPs $p_1$ and $p_2$ as well as an edge from $p_1$ to $p_2$, and $X_p^{\text{sc}}$ is an integer variable assigning an SCC number to a DP $p$. Hence $T_{\text{sc}}(k)$ encodes the separation of the graph into at most $k$ SCCs, and $T_S(i),T_W(i)$ encode conditions to orient the $i$th SCC.

Soundness of all the above encodings can be shown by relating them to their processor counterparts [3], but we omit the proofs here due to lack of space.

### 4 Experiments

We implemented our DP framework encoding in Maxcomp as described in Section 3. Besides enhancing the previous LPO and KBO implementations with argument filterings, we also added a (restricted version of) linear polynomial interpretations as reduction pair processors. Both versions of the DG processors were included as well.

Table 1 summarizes our experimental results\(^1\) for the test bed comprising 115 equational systems from the distribution of mkbTT [7]. Each ES was given a time limit of 180 seconds, timeouts are marked $\infty$. Row (1) corresponds to the original Maxcomp using LPO. In setting (2) we use a strategy combining dependency pairs with reduction pair processors applying linear polynomials and LPO. Setting (3) enhances setting (2) with a simple DG processor encoding according to Definition 6, and setting (4) uses Definition 7 with 2 SCCs instead. A simple heuristic is applied by the automatic mode (5): one iteration is run with plain LPO and setting (2) in parallel, but afterwards only one strategy (which can orient more of the initial equations) is kept. The column $\#$ lists the number of successful completions, the next column gives the average time for a successful completion.

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\(^1\) Details available from [http://cl-informatik.uibk.ac.at/software/maxcompdp](http://cl-informatik.uibk.ac.at/software/maxcompdp)
<table>
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<th>method</th>
<th>#</th>
<th>avg. time</th>
<th>CGE₂</th>
<th>proofreduction</th>
<th>equiv_proofs</th>
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<td>∞</td>
<td>∞</td>
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<td>14.7</td>
<td>6.4</td>
<td>∞</td>
<td>1.6</td>
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<td>(3) DG, poly, LPO</td>
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<td>2.8</td>
<td>∞</td>
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<tr>
<td>(4) DG/2SCCs, poly, LPO</td>
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<td>2.5</td>
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<td>∞</td>
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<tr>
<td>(5) auto</td>
<td>92</td>
<td>10.2</td>
<td>5.6</td>
<td>135.1</td>
<td>1.5</td>
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Table 1 Experimental Results.

With the relatively lightweight DP strategy (2) we successfully complete the problems mentioned in Table 1, which cannot be completed using plain LPO or KBO. However, some other systems are lost, compared to Maxcomp using LPO. Typically, these problems require many iterations and/or give rise to many equations. Thus in total (2) completes not quite as many systems as (1), and the average time is tripled. Settings (3) and (4) require considerably more encoding effort and hence succeed on comparatively few systems. For instance, only proving termination of the convergent (unreduced) TRS for equiv_proofs (74 rules) produced in a completion run with setting (2) takes 1.4 seconds for strategy (2) (12K variables, 45K clauses) but 176 seconds with setting (3) (290K variables, 1.2M clauses).

Overall the automatic mode turned out to be most powerful since it can often be efficient by applying LPO, but also switch to a more sophisticated strategy in case of unorientable equations. There are even some problems like proofreduction where (5) succeeds but (2) does not—apparently it can be preferable to apply LPO in the beginning before switching to the DP strategy.

References